




RECEIVED
FEB 11 2002
Technology Center 2100

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION PTO-1449				ATTY. DOCKET NO. SUN-P3900-SPL		APPLICATION NO. 09/761,217	
				APPLICANT Shailender Chaudhry, et al.			
				FILING DATE January 16, 2001		GROUP ART UNIT 2183	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
BJO	4,760,520	7/26/88	Shintani et al.	364	200	10/31/85	
BJO	5,919,256	7/6/99	Widigen et al.	712	218	3/26/96	
B3C	5,996,060	11/30/99	Mendelson et al.	712	205	9/25/97	
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
B3G	Publication, entitled "Value Prediction for Speculative Multithreaded Architectures," by Pedro Marcuello et al., Universitat Politecnica de Catalunya, Proceedings of the 32 nd Annual ACM/IEEE International Symposium on Microarchitecture, Israel, November 16-18, 1999, Pages 230-236.						
B3D	Publication, entitled "Available Parallelism with Data Value Prediction," by Rahul Sathe and Manoj Franklin., Proceedings of International Conference on High Performance Computing, December 17, 1998, XP001001139, Pages 194-201.						
EXAMINER 				DATE CONSIDERED 1/8/04			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.